

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.09/665,920
 Filing Date Sep 20, 2000
 Inventorship Haba et al.
 Applicant Rambus Inc.
 Group Art Unit 2841
 Examiner Phan, T.
 Attorney's Docket No.RB1-008US
 Title: Multi-Channel Memory Architecture



#6/Response
 P. Ty
 10/23/02

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RESPONSE TO 7/17/02 OFFICE ACTION

To: Commissioner of Patents and Trademarks
Washington, D.C. 20231

From: Emmanuel A. Rivera (Tel. 509-324-9256; Fax 509-323-8979)
Customer No. 29150



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PATENT & TRADEMARK OFFICE

REMARKS

Claims 1-30 were originally submitted. Claims 9, 10, and 22 are canceled.
 Claims 31-38 have been submitted in a previous response. Thus, claims 1-8, 11-
 21, and 23-38 are currently pending.

Reconsideration and allowance of all claims is respectfully requested.

Cited References

All of the submitted claims were rejected in the Office Action of 07/17/02
 based upon three references: US Patent No. 5,943,573 to Wen (hereinafter
 "Wen"), US Patent No. 4,567,545 to Mettler, Jr. (hereinafter "Mettler"), and U.S.
 Patent No. 6,049,467 to Tamarkin et al. (hereinafter "Tamarkin").

Wen describes the fabrication of a semiconductor memory device having a
 typical memory layout in which word lines extend parallel to each other across a